VIOLATIONS 3RD ED - JR17

R9 10k

M

JP1

S1
ON/OFF

LED1

78L05A

C8

47 uF

BAT

R16
1k

IC3

C9

0.1 uF

+

O

B

N

C7

47 uF

JACK

R3
1k

R8
1k

R7
10k

R2
10k

R4
1k

C2
0.1 uF

PIC18F26K22-I/P

IC1

VR1

VR2

10k

10k

value

tempo

A

B

R1

10k

C

SW1

10 nF

22 nF

47 nF

C4

C5

C6

R11
1M

R10

10k

C3

0.1 uF

R5
1k

R12
10k

R6
10k

LM358N

IC2

C1

47 uF

R13
10k

R15
10k

Q

1 filter

8

R14

100k
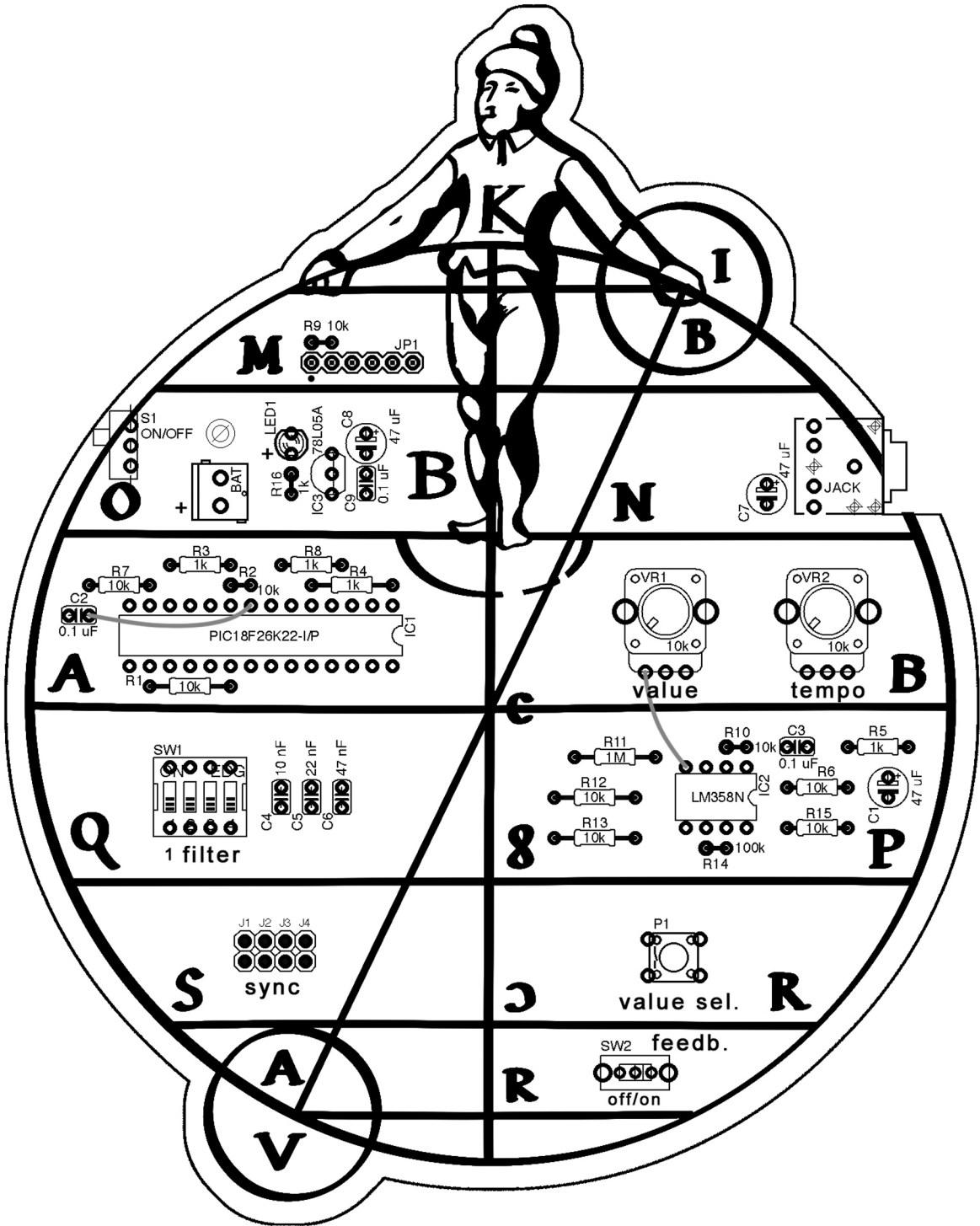
P

J1 J2 J3 J4

sync

P1

value sel.

S

C

R

A

V

SW2

feedb.

R

off/on

PARTLIST

NOTE!
R11   1M (br/blk/blk/yel/br)
R14   100k (br/blk/blk/org/br)
ICs and sockets (notch top)
Caps polarity C1, C7, C8 (short leg -)
LED polarity (short leg/flat side -)
Resistors no polarity (any direction)

IC1   PIC18F26K22 (pic) (notch top)
IC2   LM358 (op amp) (notch top)
IC3   78L05A (volt reg. +5V)

DIL28   Socket (x1) (notch top)
DIL8   Socket (x1) (notch top)

S1   SWITCH-SPDT (side) (on/off)
SW1   DIP-4
SW2   SWITCH-SPDT (top)

C1   47 u (x3) polarity (short leg -)
C2   0.1 u (100 n) (x3) (beige 104)
C3   0.1 u (100 n) (beige 104)
C4   10 n (beige 103)
C5   22 n (beige 223)
C6   47 n (beige 473)
C7   47 u polarity (short leg -)
C8   47 u polarity (short leg -)
C9   0.1 u (100 n) (beige 104)

R1   10k (x9) (br/blk/blk/red/br)
R2   10k
R3   1k (x5) (br/blk/blk/br/br)

R4   1k
R5   1k
R6   10k
R7   10k
R8   1k
R9   10k
R10   10k
R11   1M (br/blk/blk/yel/br)
R12   10k
R13   10k
R14   100k (br/blk/blk/org/br)
R15   10k
R16   1k

VR1   10k (x2) (pot)
VR2   10k

LED1   3mm (short leg/flat side -)
P1   tactile switch

J1-J4   4+4 double row header (sync)
J3   Closed jumper (black)
JP1   6-way header (ICSP)

JACK   3.5 mm socket (mono summed)
BAT   Screw terminal (bat)

Feet (x4)
Battery clip
Jumper wire (x2)

More info and documentation:
www.dirtyelectronics.org

The array length – number of steps in the sequence to be looped – is built-up incrementally (1 step, 2 step etc.) by selecting and writing a value with VR1 and P1. When the maximum length of the array (sequence) is reached, the array is cleared and the process begins a new. The maximum length of the array is set in the code: default 16.

To 'violate' the process, switch SW2 on and hold P1. A self-generating sequence will be created.

SW1 filter (and feedback)
1 full/half 'voice' (feedback)
2 – 4 low-pass filters

External syncing
Violations' sequencer can be externally synced and triggered using a control voltage or pulse. External jack socket/patchbay connector required.

J1 Tempo CV 0 - +5V (top/+; bottom/-)
J2 Trigger CV 0 - +5V (top/+; bottom/-)
J3 Internal clock/tempo control (remove when in external sync operation)
J4 Jumper holder

Note! For internal tempo control J3 (closed jumper/black) must be inserted.